



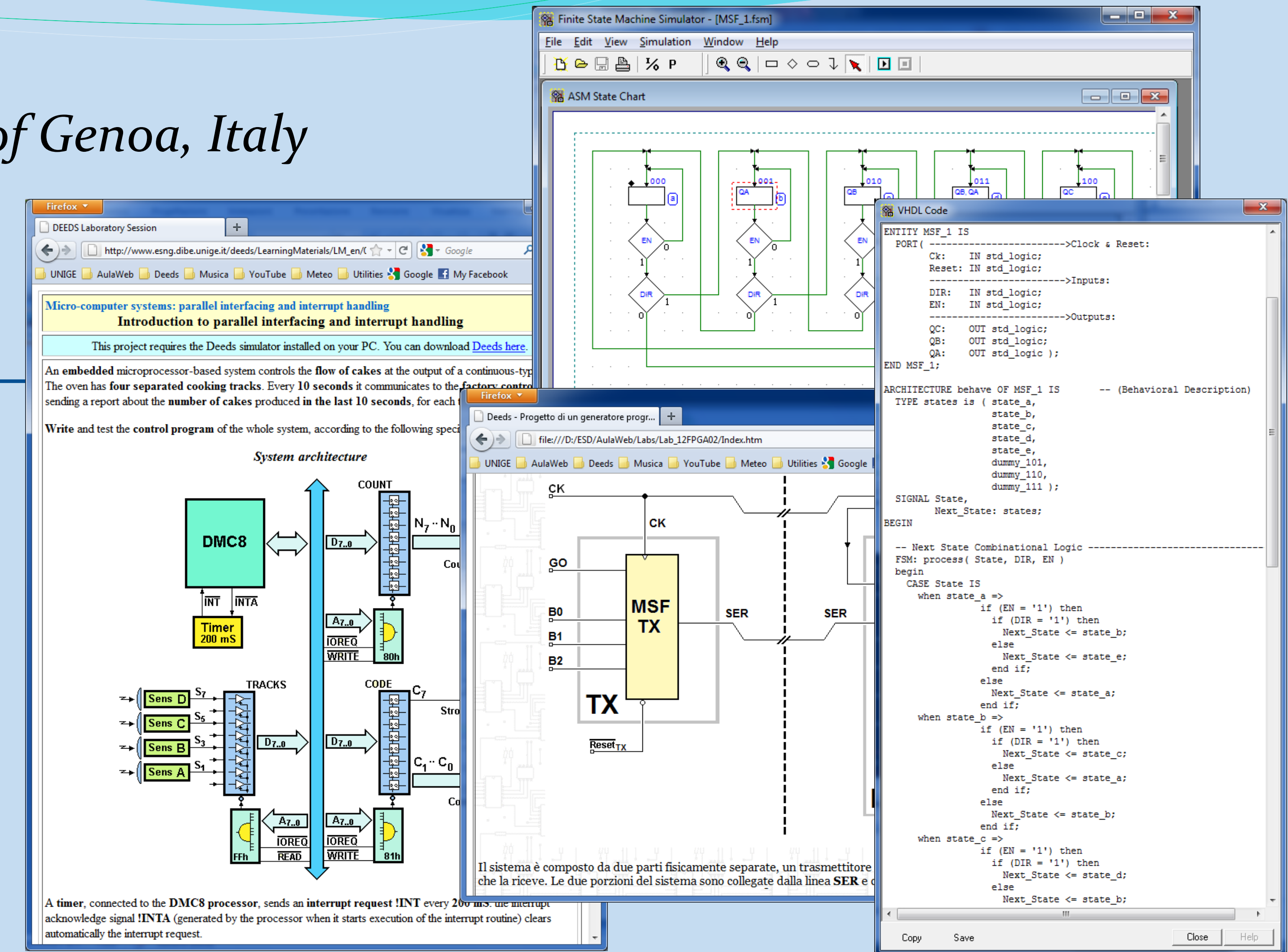
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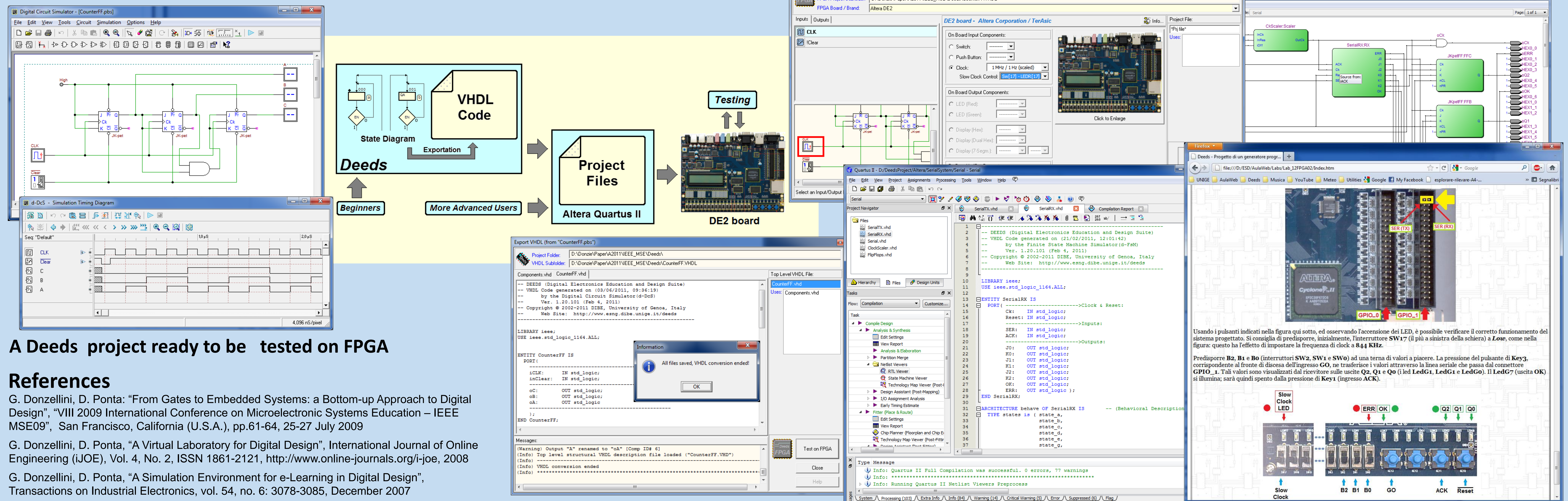
***<http://www.esng.dibe.unige.it/deeds>***

## Deeds (Digital Electronics Education and Design Suite):

- Deeds is a learning environment based on three integrated simulators with good features and a simple user interface.
- Deeds is associated with a repository of learning material and application projects, available on the web.
- Deeds guides learners from simple gates circuits to the hardware/software foundations of embedded systems.
- A new extension allows to export a project in VHDL format and to test in a FPGA board.
- An Expert allows the choice of the FPGA board and its configuration for the circuit under test.
- Beginner learners can test Deeds projects without a previous knowledge of VHDL and FPGA proprietary software.
- More advanced students still have the choice of interacting directly with the FPGA development system.



## Examples of Deeds projects exported in VHDL and tested on FPGA



## The Deeds VHDL generator

**The Deeds FPGA Expert (top) and the project opened in Altera Quartus II© (bottom)**

**RTL view of the project (top) and on board testing on the Terasic DE2 (bottom)**