Ck	1	3	4	5	6	7	8	9	10	<b>1</b> 1	12	13	14	15	16	<b>1</b> 7	18	19	20	21	22	23	24
Reset			 																				
Controller				ľ		1	1	1				1		1	I	I	Ĭ	1	1				
ON																							
UP																							
MID									1														
DN																							
OFF																							
P7	 		 																				
PD	 		 																				
LD			 																				
UPD																							
EN	 		 																				
Q7Q0			 																				
MIN	 		 																				
MAX	 		 																				
PRS	 		 																				

## Chapter 8 – Exercise 1 – Design of the controller of a given datapath – Timing diagram to be completed