t 14 15 19 20 22 10 12 13 16 17 18 21 3 11 23 24 1 2 4 5 6 7 8 9 Ck Reset **D0** D1 D2 GO Controller P3..P0 Q3..Q0 ··· .2 тс DFF EFF SER

Chapter 8 – Exercise 2 – Design of the controller of a given datapath – Timing diagram to be completed