| ск | | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 10 | 1 1 | 12 | 13 | 14 | 15 | 16 | 17 | 18 | 19 | 20 | 21 | 22 |
|------------|--|---|---|---|---|---|----------|---|---|----|------------|----|----|----|----|----|----|----|----|----|----------|----|
| Reset | | | | | | | | | | | | | | | | | | | | | | |
| UP | | | | | | | | | | | | | | | | | | | | | | |
| DN | | | | | | | | | | | | | | | | | | | | | | |
| Controller | | | 1 | 1 | 1 | 1 | <u> </u> | 1 | 1 | 1 | <u> </u> | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | <u> </u> | |
| MIN | | | | | | | | | | | | | | | | | | | | | | |
| MAX | | | | | | | | | | | | | | | | | | | | | | |
| V3 | | | | | | | | | | | | | | | | | | | | | | |
| V2 | | | | | | | | | | | | | | | | | | | | | | |
| V1 | | | | | | | | | | | | | | | | | | | | | | |
| V0 | | | | | | | | | | | | | | | | | | | | | | |
| ENR | | | | | | | | | | | | | | | | | | | | | | |
| PRS | | | | | | | | | | | | | | | | | | | | | | |
| Q3 Q2 | | | | | | | | | | | | | | | | | | | | | | |
| Q2 | | | | | | | | | | | | | | | | | | | | | | |
| Q0 | | | | | | | | | | | | | | | | | | | | | | |
| Q U | | | | | | | | | | | | | | | | | | | | | | |

Chapter 8 – Exercise 5 – Design of the controller of a given datapath – Timing diagram to be completed